

## CA Seminar

### CSE-B

<b>S.No</b>	<b>Name</b>	<b>Roll No.</b>	<b>Seminar Topic</b>
<b>1</b>	<b>Lovedeep Kaur</b>	<b>2018/358</b>	<b>General Register Organisation</b>
<b>2</b>	<b>Lovepreet Singh</b>	<b>359</b>	
<b>3</b>	<b>Lovish Bhatia</b>	<b>360</b>	
<b>4</b>	<b>Manav Raj</b>	<b>362</b>	<b>Stack Organisation</b>
<b>5</b>	<b>Manoj Sharma</b>	<b>363</b>	
<b>6</b>	<b>Mehakpreet Singh</b>	<b>365</b>	<b>Addressing Modes</b>
<b>7</b>	<b>Mohit Dhiman</b>	<b>366</b>	
<b>8</b>	<b>Molik</b>	<b>367</b>	
<b>9</b>	<b>Monika</b>	<b>368</b>	<b>RISC/GSC</b>
<b>10</b>	<b>Neha Garg</b>	<b>369</b>	

<b>11</b>	<b>Nipun Gandhi</b>	<b>370</b>	
<b>12</b>	<b>Nisha</b>	<b>371</b>	<b>IP Modes Of Data Transfer</b>
<b>13</b>	<b>Paramvir</b>	<b>372</b>	
<b>14</b>	<b>Parth Batta</b>	<b>373</b>	
<b>15</b>	<b>Preeti</b>	<b>374</b>	<b>FIFO Buffer</b>
<b>16</b>	<b>Pushpinder</b>	<b>375</b>	
<b>17</b>	<b>Raghav Gupta</b>	<b>376</b>	
<b>18</b>	<b>Rahul Sharma</b>	<b>377</b>	<b>DMA Controller</b>
<b>19</b>	<b>Rahul Verma</b>	<b>379</b>	
<b>20</b>	<b>Raju Singh</b>	<b>380</b>	<b>DMA Transfer</b>
<b>21</b>	<b>Ranbir Singh</b>	<b>382</b>	<b>Auxiliary Memory</b>
<b>22</b>	<b>Rohit Kumar</b>	<b>383</b>	
<b>23</b>	<b>Saisha</b>	<b>385</b>	
<b>24</b>	<b>Sandeep Kumar</b>	<b>386</b>	
<b>25</b>	<b>Sarabjot Gill</b>	<b>387</b>	

<b>26</b>	<b>Satyam</b>	<b>388</b>	<b>Associative Memory</b>
<b>27</b>	<b>Sheenu</b>	<b>389</b>	
<b>28</b>	<b>Shivam</b>	<b>390</b>	
<b>29</b>	<b>Shivam Gupta</b>	<b>391</b>	
<b>30</b>	<b>Simarbir Sandhu</b>	<b>392</b>	
<b>31</b>	<b>Simranjeet Singh</b>	<b>395</b>	<b>Virtual Memory</b>
<b>32</b>	<b>Sparsh</b>	<b>396</b>	
<b>33</b>	<b>Sukhpreet Singh</b>	<b>397</b>	
<b>34</b>	<b>Tajbir</b>	<b>398</b>	
<b>35</b>	<b>Tushar Arora</b>	<b>400</b>	
<b>36</b>	<b>Viraj</b>	<b>401</b>	
<b>37</b>	<b>Yadhwinder</b>	<b>405</b>	<b>Cache Memory</b>
<b>38</b>	<b>Yatin</b>	<b>406</b>	
<b>39</b>	<b>Karandeep Singh</b>	<b>2019/440</b>	
<b>40</b>	<b>Mahabali</b>	<b>441</b>	

<b>41</b>	<b>Pardeep Kumar</b>	<b>443</b>	<b>Memory Management Hardware</b>
<b>42</b>	<b>Piyush Vashisht</b>	<b>444</b>	
<b>43</b>	<b>Rajneesh</b>	<b>446</b>	
<b>44</b>	<b>Rose</b>	<b>447</b>	
<b>45</b>	<b>Satwinder Singh</b>	<b>449</b>	<b>Memory Heirarchy, RAM ROM Chips</b>
<b>46</b>	<b>Shabnam Thakur</b>	<b>450</b>	
<b>47</b>	<b>Vikas Kumar</b>	<b>451</b>	
<b>48</b>	<b>Vikas Sharma</b>	<b>452</b>	